



**A LOW LATENCY INFRASTRUCTURE FOR
5 NINE'S TRADING ECOSYSTEM
TECHNICAL WORKSHOP FOR DEVELOPERS**

Thursday, 28 May 2009
0900 – 1500
Seminar Room 5.2 (Level 5)
School of Social
Science/Economics
Singapore Management
University
80 Stamford Road
Singapore 178902

0900 - 0915	<i>Reception</i>
0915 – 0920	INTRODUCTION Ian Pearl, Sun Microsystems
0920 – 1050	PARALLEL PROGRAMMING FOR PERFORMANCE David Shone, Sun Microsystems
1050 – 1100	<i>Coffee Break</i>
1100- 1130	BUILDING A HIGH-PERFORMANCE MESSAGING INFRASTRUCTURE 29West
1130 - 1200	HOW TO ACHIEVE SCALE OUT AND LOW LATENCY Gigaspace
1200– 1230	OPTIMISING PERFORMANCE ON INTEL NEHALEM Intel
1230 – 1330	<i>Light Lunch</i>
1330 – 1345	DATA ACCESS DOESN'T HAVE TO BE SLOW Nigel Chng, Sun Microsystems
1345– 1415	MONITORING THE HEALTH OF TRADING SYSTEMS Misha Kipnis, ITRS
1415 – 1500	TIPS FOR TUNING TO ELIMINATE LATENCY AND JITTER Amjad Khan, Sun Microsystems